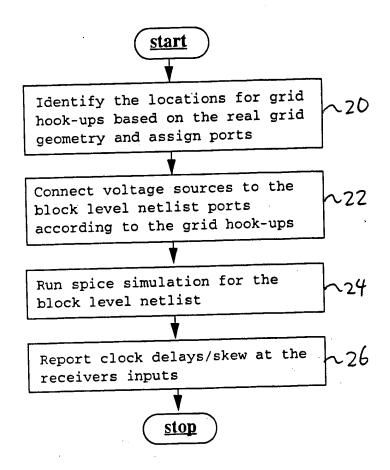


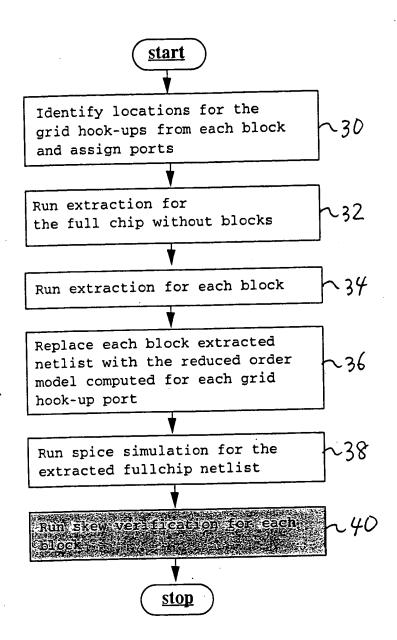
F1G. 1

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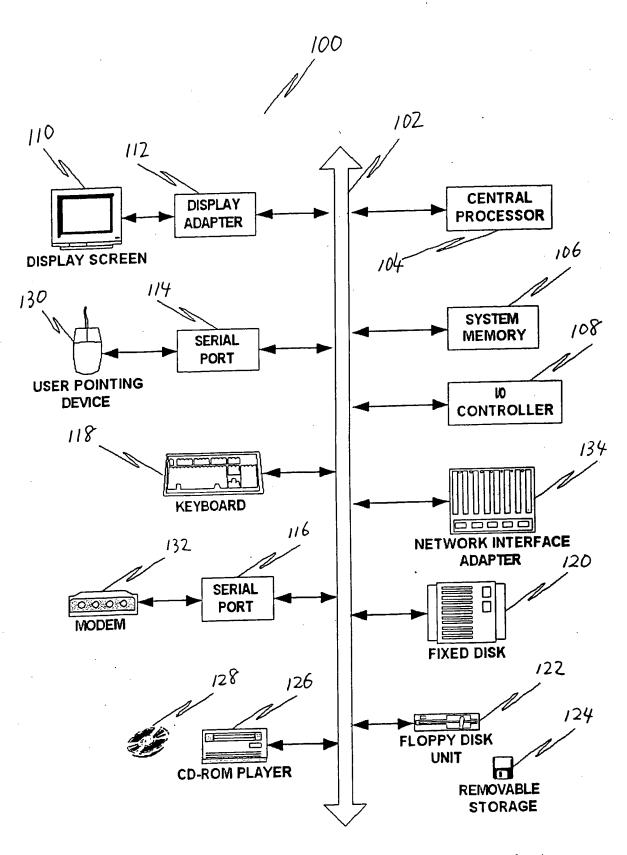


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F19. 2



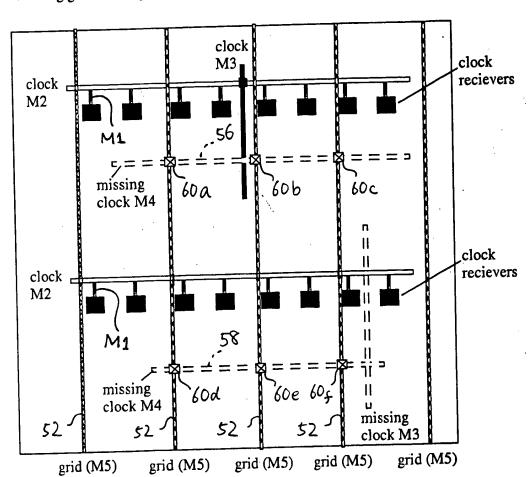
F19.3



F16.4

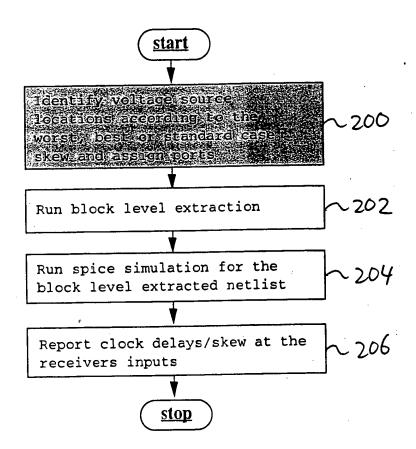
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missing grid hook-ups

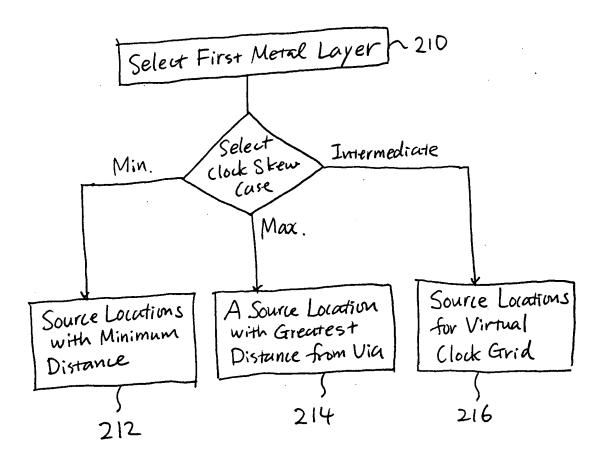


F19.5

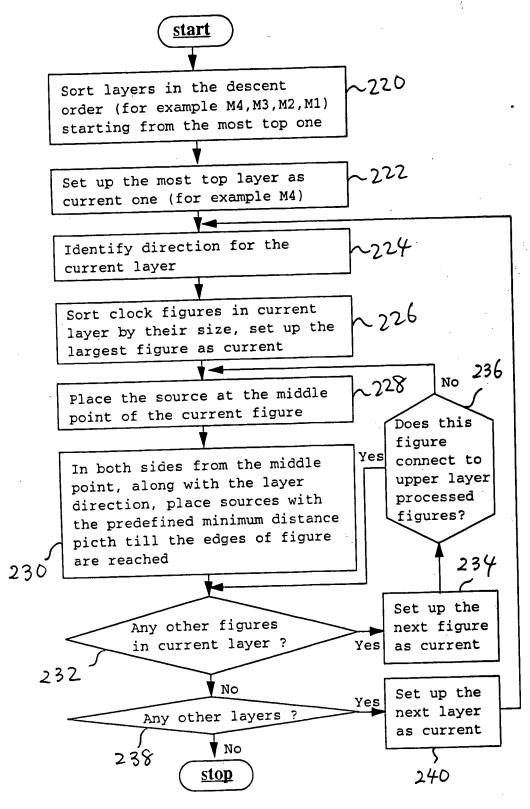
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F1G. 6

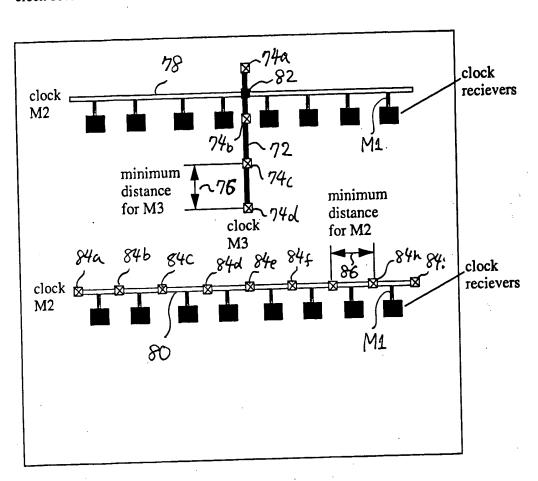


F14.7



F19.8

clock source locations for minimum skew



F19.9

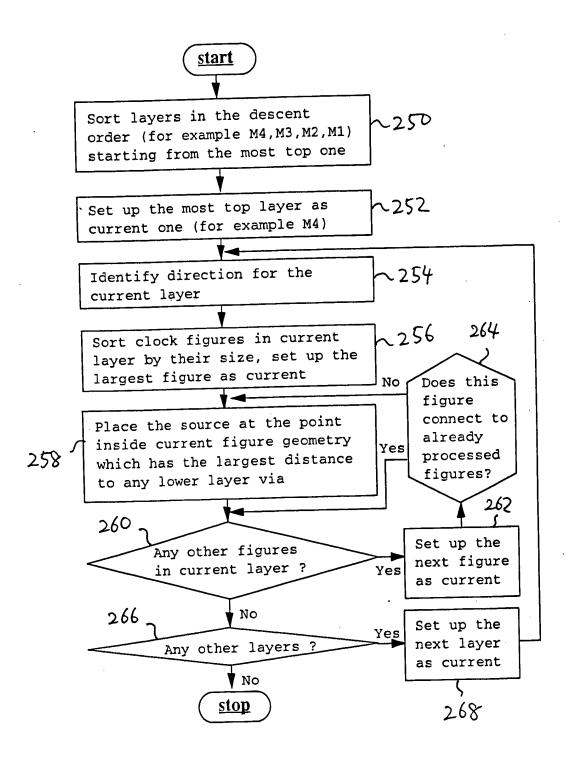


FIG. 10

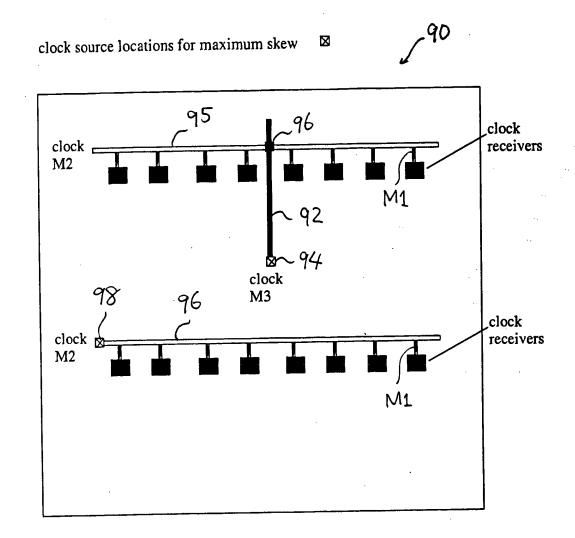
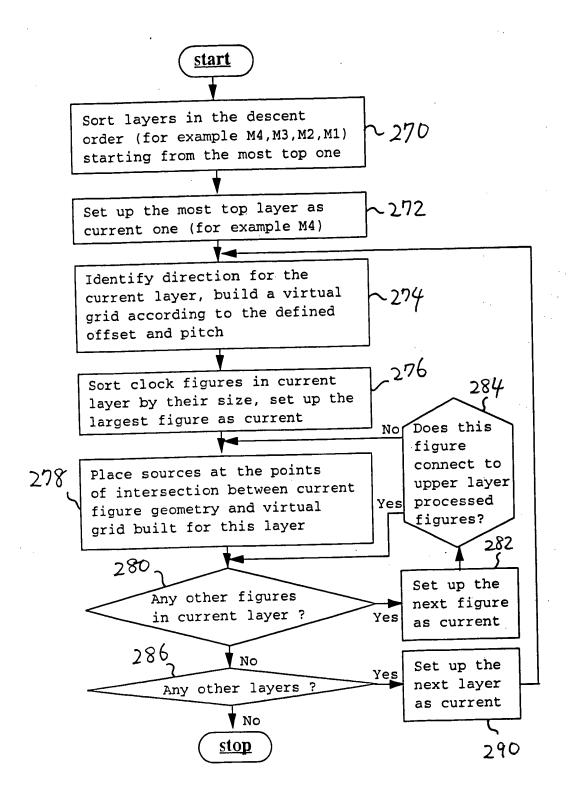


FIG. 11



F19.12

200 clock source locations for medimum skew -308 310 304a offset M3 clock clock c receivers M2 M1 306 pitch M3 302 304b clock M3316b 316 C 312 3160 clock clock c receivers M2

MI

F1G. 13

pitch M2

offset M2

318

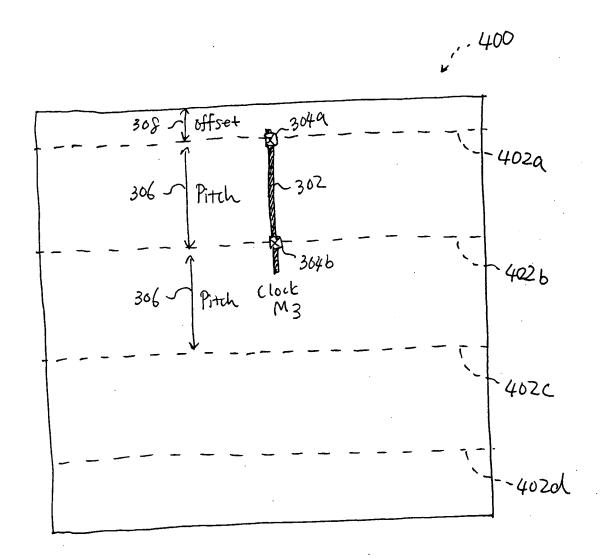


FIG. 14

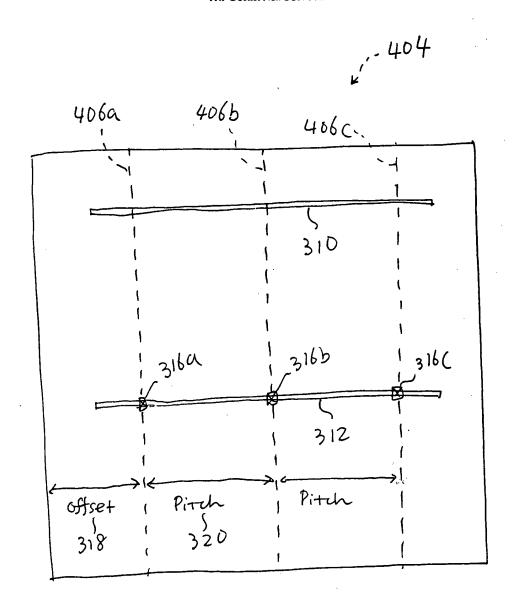
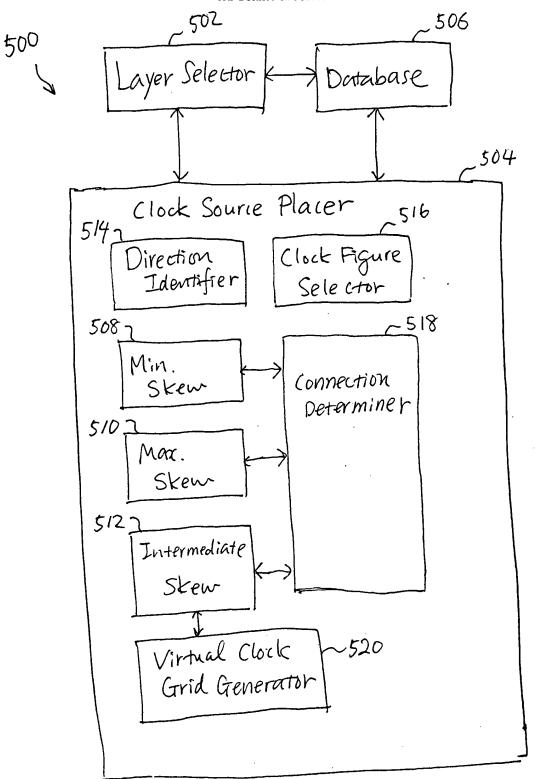


FIG. 15



F19.16